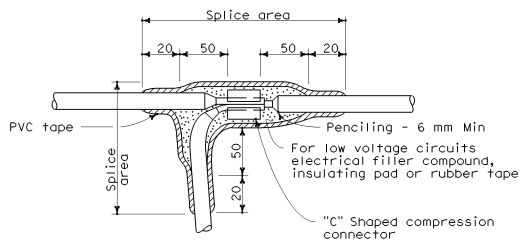


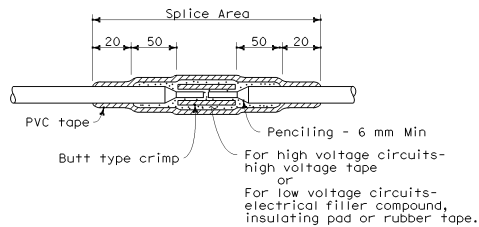
### TYPE "C" SPLICE

Between 1 free-end and 1 through conductor



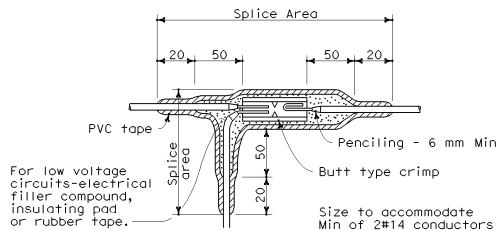
### TYPE "T" SPLICE

For 3 free-ends



### TYPE "S" SPLICE

Between 2 free-ends



### TYPE "ST" SPLICE



DIST.	COUNTY	ROUTE	KILOMETER POST TOTAL PROJECT	SHEET TOTAL NO. SHEETS

*Martha V. Stryer*  
REGISTERED ELECTRICAL ENGINEER

July 1, 2004  
PLANS APPROVAL DATE

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To get to the Caltrans web site, go to: <http://www.dot.ca.gov>

Martha V. Stryer  
No. E13738  
Exp. 6-30-07  
REGISTERED PROFESSIONAL ENGINEER  
ELECTRICAL  
STATE OF CALIFORNIA

### NOTES

1. Dimensions are minimum.
2. Rubber tapes shall be rolled after application.

### INSULATION METHODS

#### Low Voltage Circuits (0-600 V)

##### METHOD "B"

1. Completely cover the splice area with electrical insulating coating and allow to dry.
2. Apply 2 layers of electrical insulating pad with minimum thickness of 4 mm each layer or 2 layers, half lapped, synthetic oil resistant, self fusing rubber tape.
3. Apply 3 layers half lapped polyvinyl chloride tape.
4. Cover entire splice with electrical insulating coating and allow to dry.

#### High Voltage Circuits (Over 600 V)

1. Completely cover the splice area with electrical insulating coating and allow to dry.
2. Apply high voltage tape to a minimum thickness equal to original insulation.
3. Apply 3 layers half lapped polyvinyl chloride tape.
4. Cover entire splice with electrical insulating coating and allow to dry.

STATE OF CALIFORNIA  
DEPARTMENT OF TRANSPORTATION  
**ELECTRICAL SYSTEMS  
(SPlicing DETAILS)**

NO SCALE  
ALL DIMENSIONS ARE IN  
MILLIMETERS UNLESS OTHERWISE SHOWN

**ES-13A**